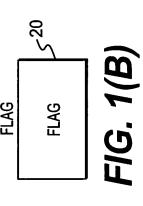
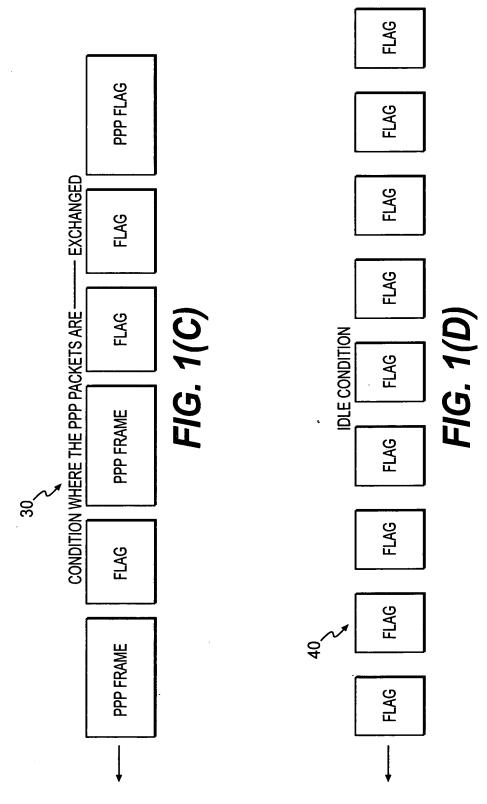
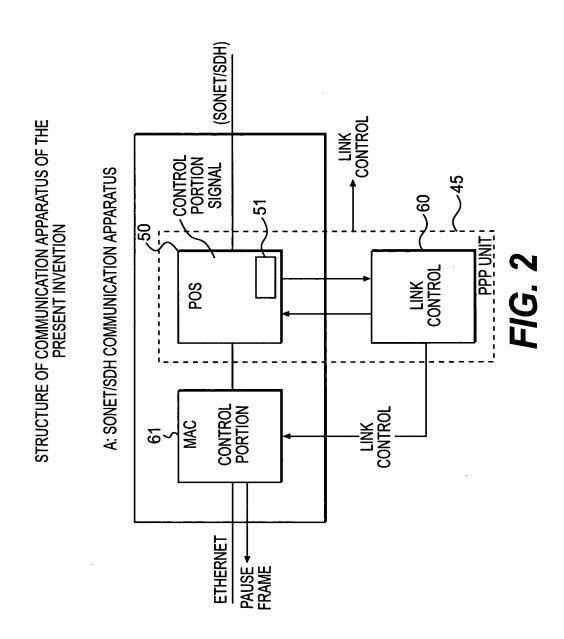


	FLAG							
	FCB							
NVENTION JH	INFORMATION USER DATA							
DIAGRAM OF THE PRESENT INVENTION PPP OVER SONET/SDH	PROTOCOL							
DIAGRAM PI	CONTROL							
	ADDRESS							
	(A) FLAG							









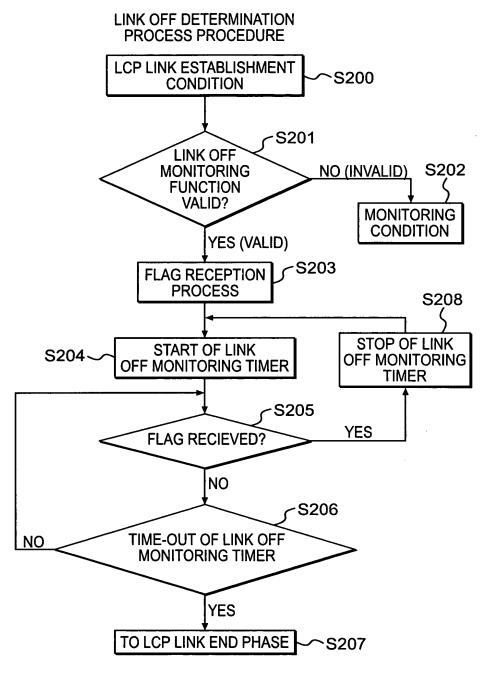
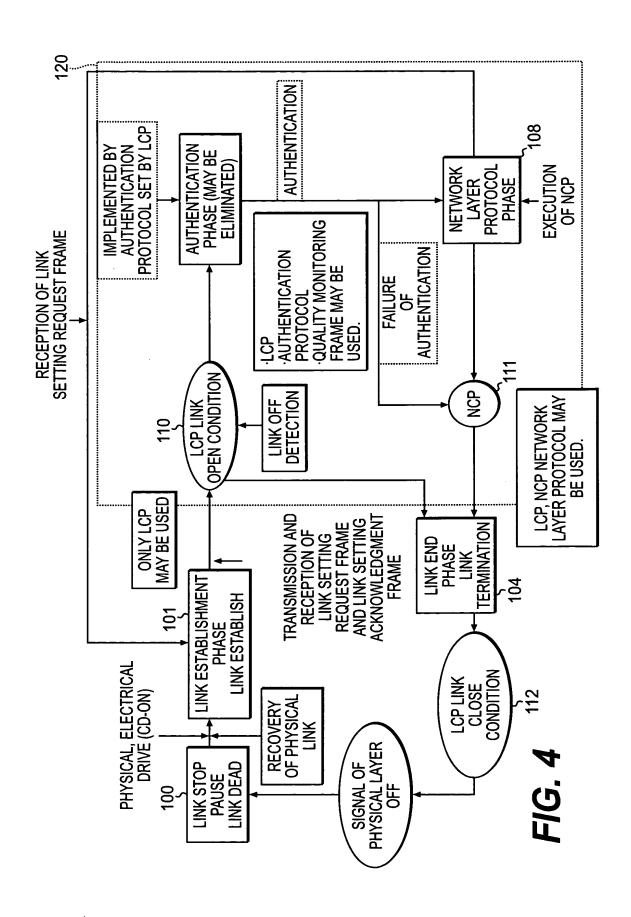
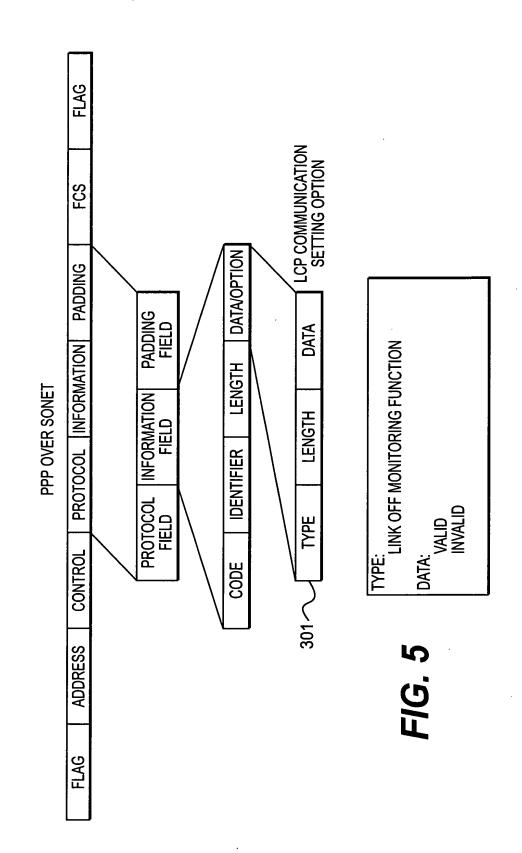
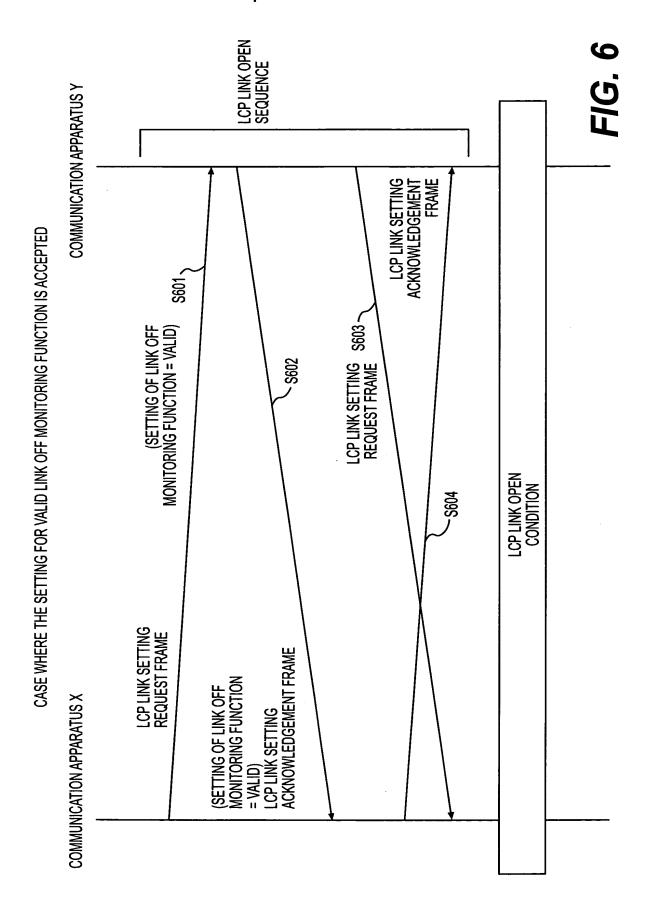
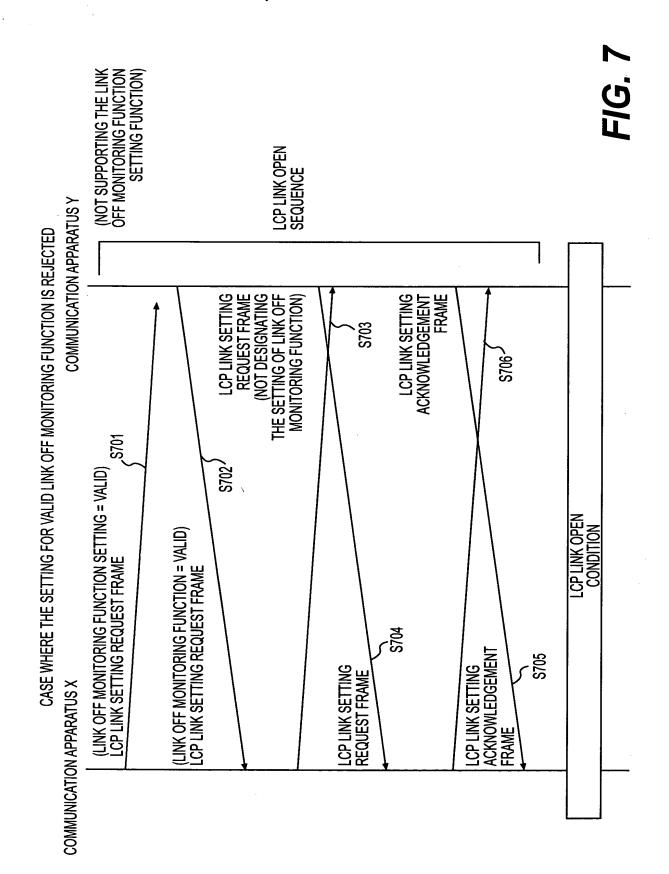


FIG. 3

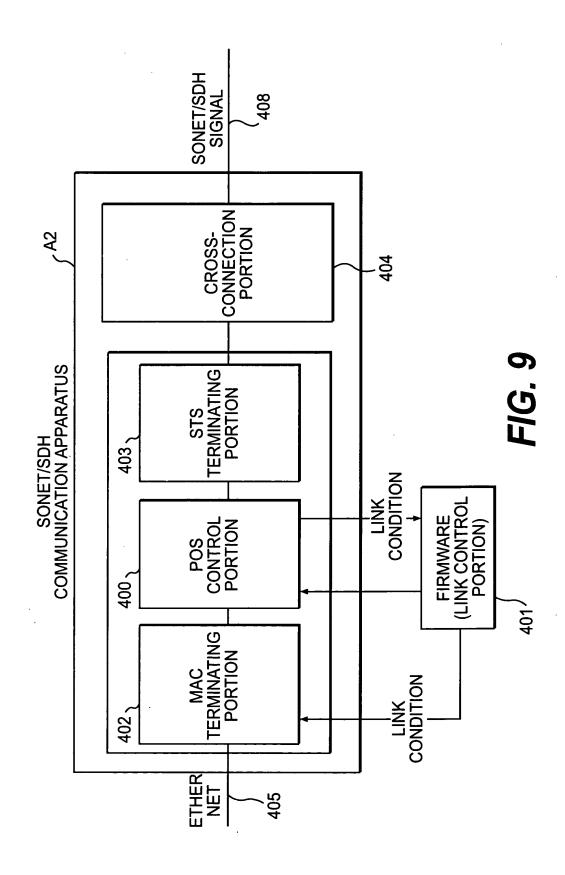


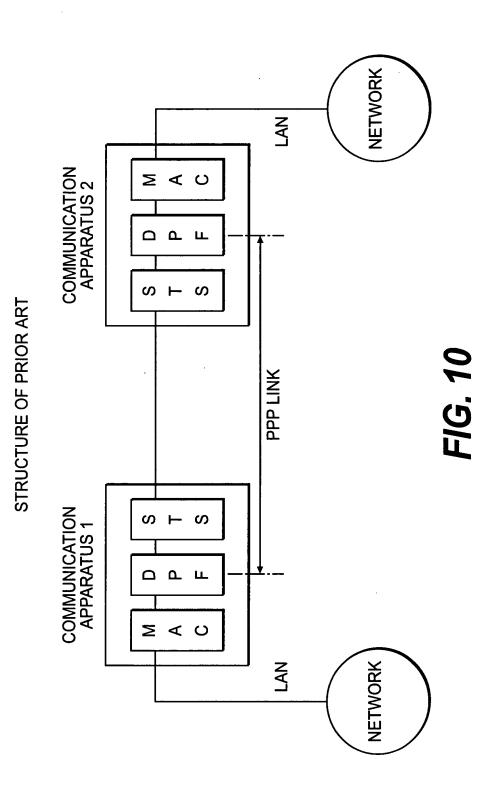


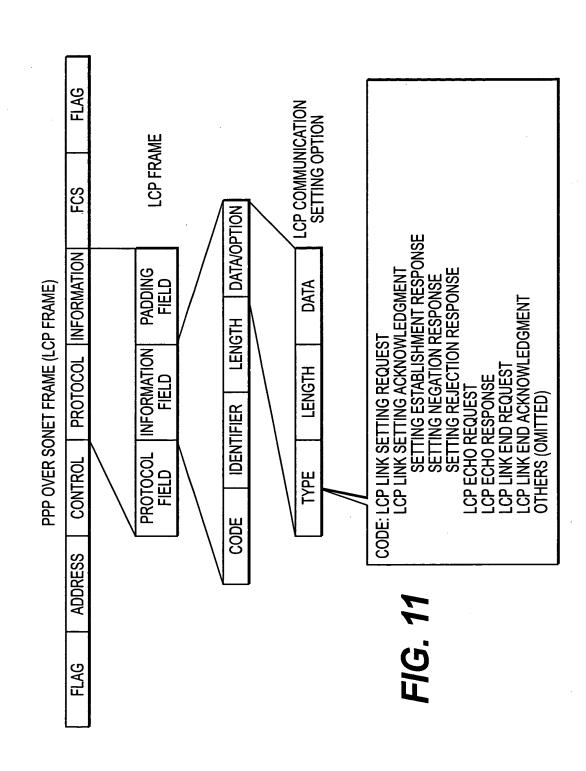




	FCS	4(0CTET)																		
	PADDING	42															(	7.C. &		
PAUSE FRAME FORMAT	INTERMISSION TIME	2																ĭ		1
	MANIPULATION CODE	2																		
	LENGTH/ TYPE	2											N TIME	(肥					: LOCT	
	Transmission Source Address	9	<u></u>	5			~			E-*(10MBPS)	5.12 μSEC for 100 BASE - * (100MBPS)	SE - * (1GBPS)	AND THE MAXIMUM VALUE OF THE INTERMISSION TIME	<b>OLLOWING INTERMISSION TIMES MAY BE</b>		OMBPS)	(100MBPS)	(1GBPS)	HERE INTERMISSION TIME = 0 IS USED TO INSTRUCT	MISSION.
	SFD DESTINATION ADDRESS	9	ION TIME SION OF MAC FRAME	TTED ONLY FOR THE	ED	512 BITS MEANS THE TIME	FOR TRANSMISSION	OF 512 BITS IN THE DESIRED	RATE.	51.2 µSEC for 10 BASE - * (10MBPS)	SEC for 100 BAS	12 $\mu$ SEC for 1000 BASE - $st$ (1GBPS)	IUM VALUE OF .	<b>DWING INTERM</b>		FOR 10 BASE - * (10MBPS)	0~330 µSEC FOR 100 BASE - * (100MBPS)	1000 BASE - 米(	SION TIME = 0	ART OF THE TRANSMISSION
	SFD	<b>-</b>	SION TIME ISSION OF	AITTED SIGNAL		MEANS		TS IN I	SSION	51.2 μ	$5.12 \mu$	512 µ	MAXIN	I I	<u>记</u>	C FOR	등 등	C.F.OR	ERMIS	TART (
	PREAMBLE	7	INTERMISSI (TRANSMIS	IS INTERMIT	TIME X 512	512 BITS	REQUIRED	OF 512 BI	TRANSMISSION RATE	SINCE			AND THE	IS 65.535, FI	DESIGNATED	0~0.33 SEC	0~330 μSE	0~33 µSE(	HEREINT	THE RE-ST/







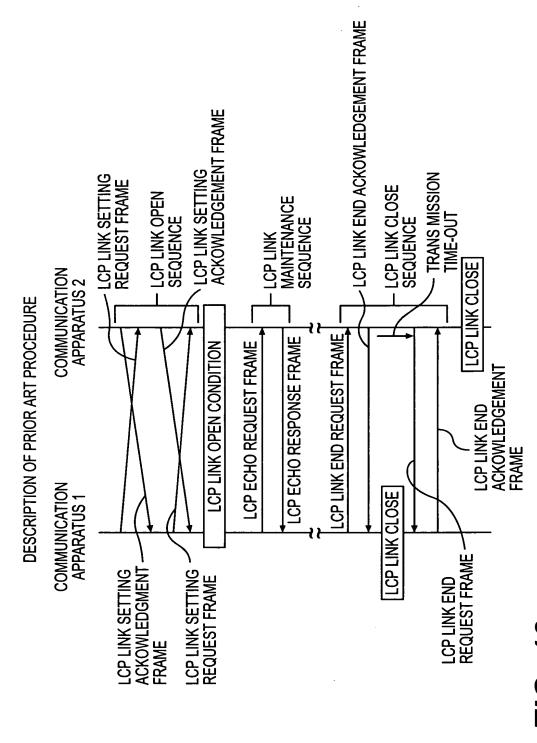


FIG. 12